

FIG. 1

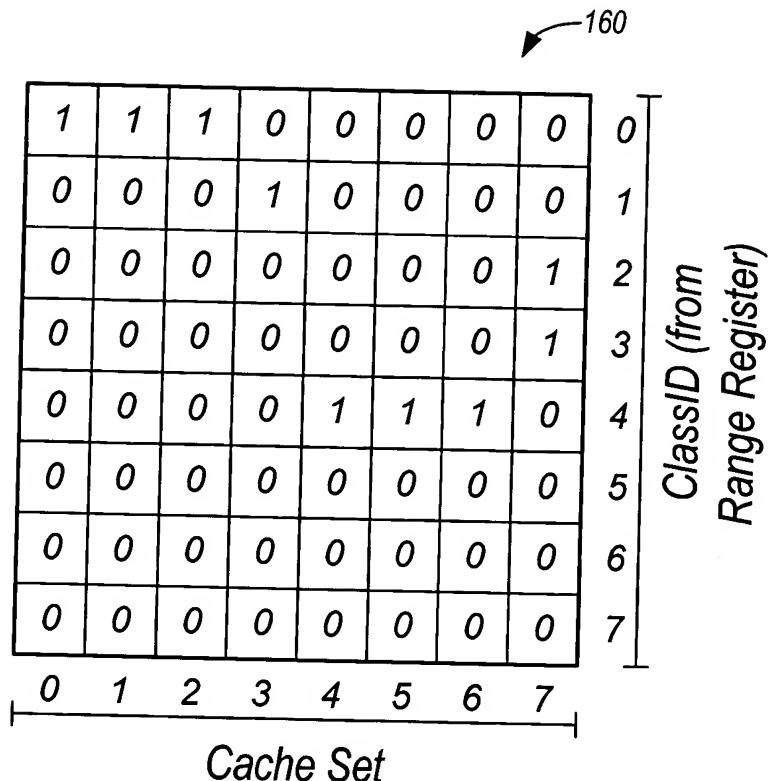


FIG. 2

Replacement Management Table:			
RMT_index	RMT_entry		
RMT_index 0	Set Enable Bits	v	b
RMT_index n	Set Enable Bits	v	a

FIG. 3

Range Start Register (RSR)

Range Start Register (high):							
0		7	8		15	16	23

Upper 32-bits of the starting address

Range Start Register (low):

32			39	40		47	48
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Lower 20-bits of the starting address

FIG. 4

Range Mask Register

Range Mask Register:							
32					47	48	50

Lower 20-bits of the starting address

FIG. 5A

ClassID Register (CIDR)

Class IDs:							
0		7	8		15	16	23

FIG. 5B

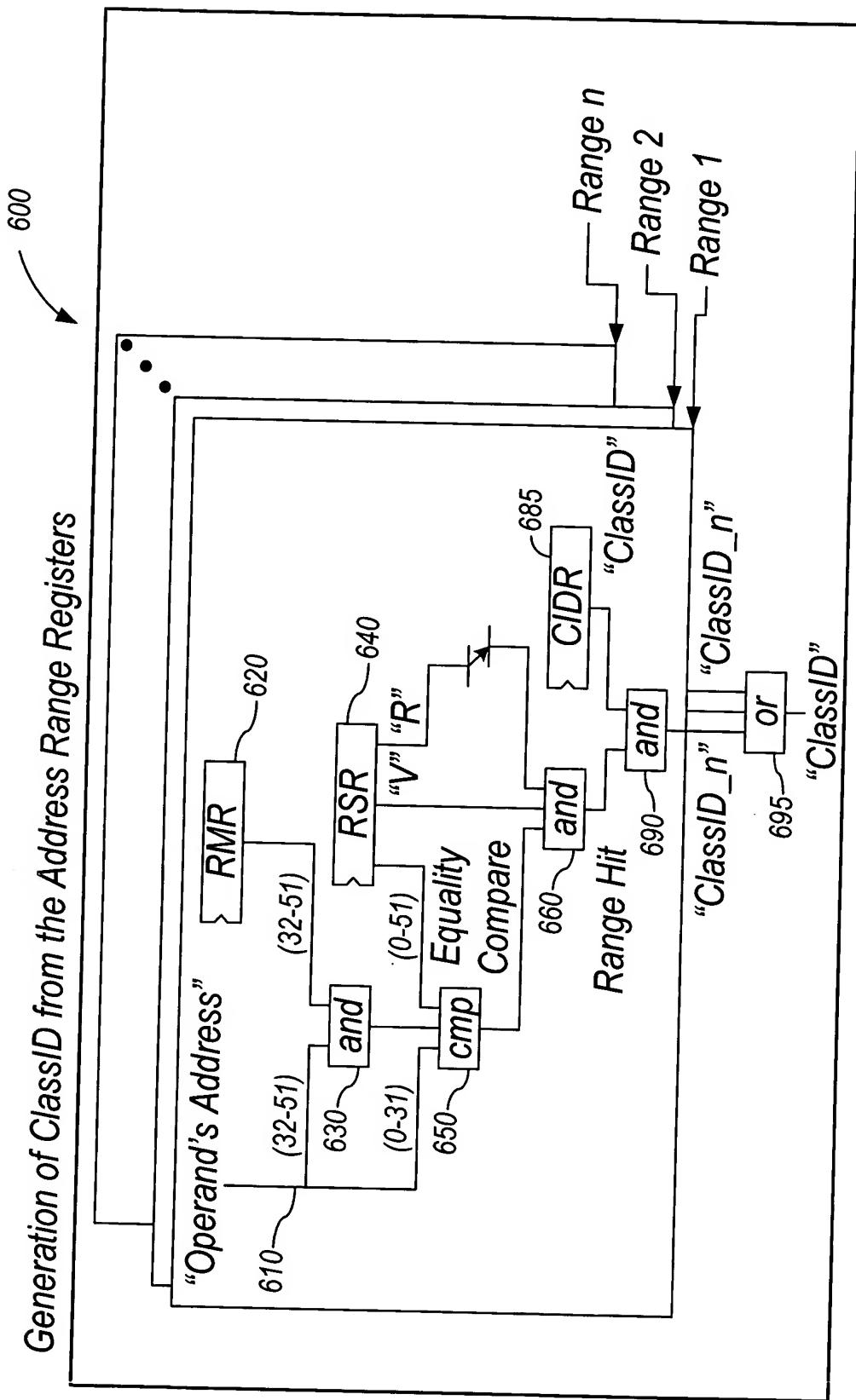


FIG. 6

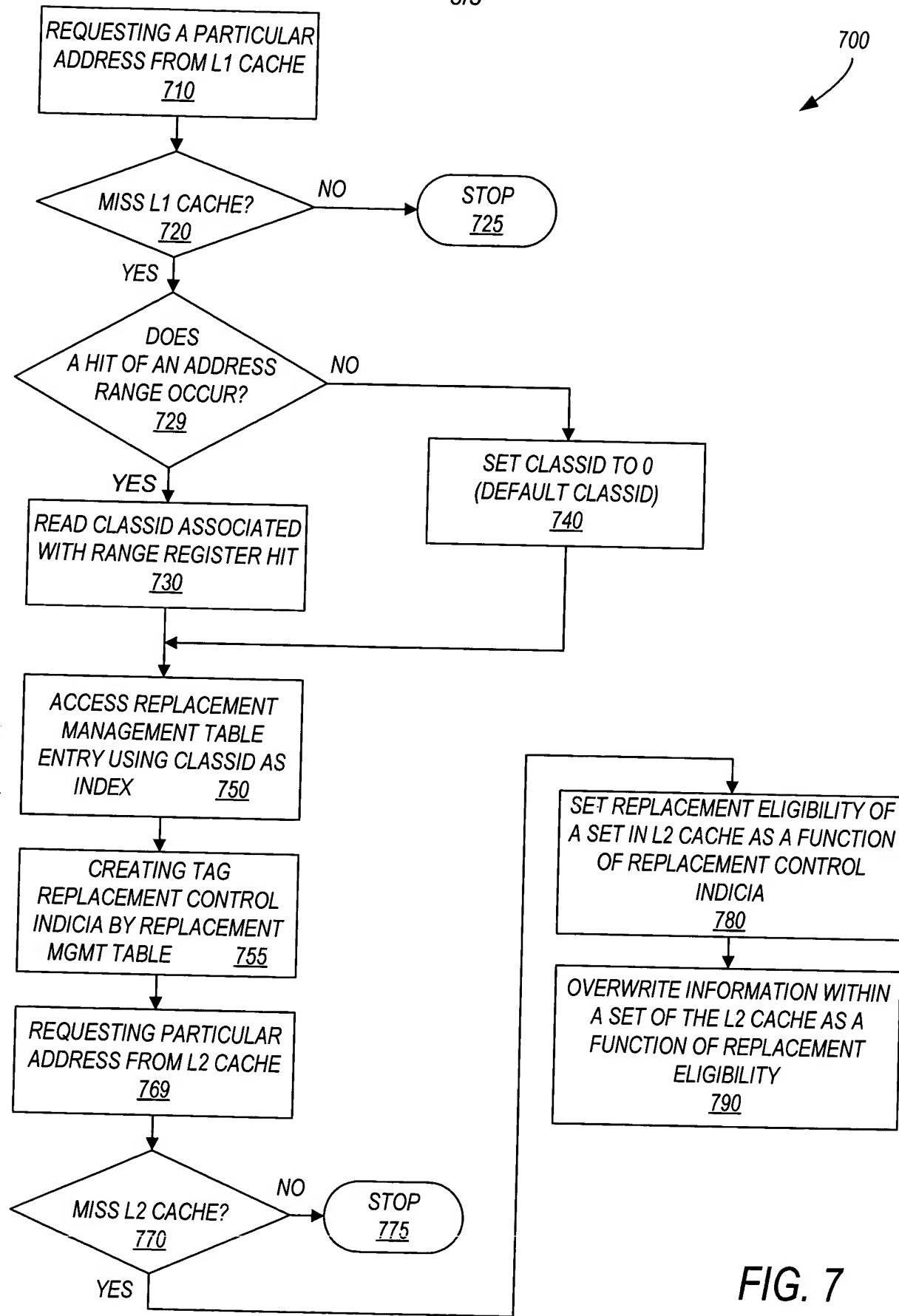


FIG. 7